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Design the architecture
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DAC: 12.15 A bias rris generator is required to generate 16 steps from 0 to 100 µA to bias an amplifier. Design a CMOS DAC to do this, assuming the presence of a 50 µA reference current. 12.16 Differential circuits provide good noise immunity and have the advantage of dual rail inputs and outputs.

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Neil H. E. Weste (born 1951), is an Australian inventor and engineer,

noted for having arris designed a 2-chip wireless LAN implementation and for authoring the textbook Principles of CMOS VLSI Design. He has worked in many aspects of integrated-circuit design and was a cofounder of Radiata Communications.

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